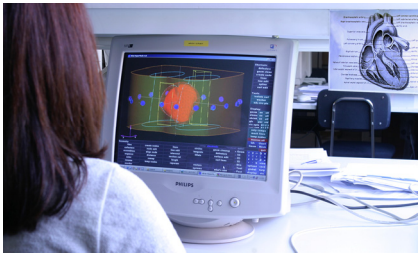


A unique opportunity

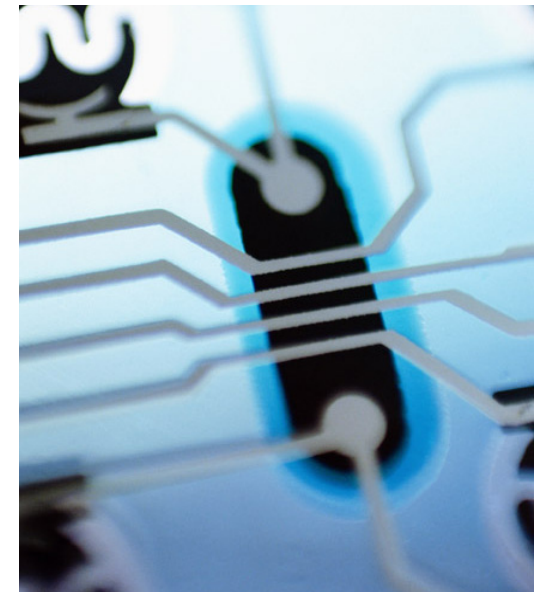
The 1st Barcelona Forum on Ph.D. Research in Electronic Engineering will bring together specialist from several large national and international companies and research institutions (STMicroelectronics, CERN, CEA, FICOSA, Vestas) and Master and Ph.D. students and professors from the Electronic Engineering Department of UPC for discussing their respective industrial and academic research activities and priorities and the role and opportunities for Master and Ph.D. students after graduation.



Auditori de l'Edifici Vèrtex in the Campus NORD
PLAÇA EUSEBI GÜELL, 6
08034 BARCELONA



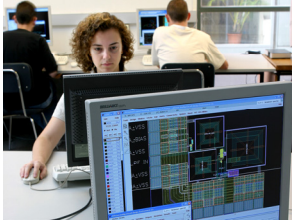
1st Barcelona Forum on Ph.D. Research in Electronic Engineering



October 16th, 2009

Universitat Politècnica de Catalunya
Campus Nord—Edifici Vèrtex

1st Barcelona Forum on Ph.D. Research in Electronic Engineering



The Universitat Politècnica de Catalunya Doctoral Program in Electronic Engineering and the Master of Electronic Engineering (along its research track) provide the

students with a broad range of research education and experience for applications in the fields of power generation, distribution and conversion, integrated circuits and systems design (digital, analog and RF), sensors and actuators, bio-engineering, semiconductor devices and microsystems.

The meeting is organized in two sessions. During the morning several representatives of innovative industries and research centers will debate the role of Ph.D. graduated students in their organizations and the most promising research axis for the next years. Next, a selection of the research results of Master and Ph.D. students of the Electronic Engineering Department will be presented and the most recent research activities of the several Department research groups will be showcased.

In the afternoon, the students will have the opportunity of a closer contact with the industry and research centers representatives during coffee table meetings.

Agenda

Morning session (open to the public)

Auditori Vertex

- 9:30 Welcome speech .
- 9:45 Panel: "The role of the Ph.D. graduated students and the international R+D priorities".
- 11:00 Coffee break.
- 11:15 Students' research work presentations.
- 13:00 Conclusions and wrap-up.

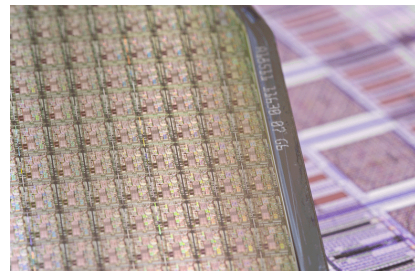
Afternoon session (for students)

Classrooms in Vertex building

- 14:30-15:30 Coffee table meeting with industry and research center representatives.

Panelists:

- Patrick Coge, STMicroelectronics
- Christian Gamrat, CEA-LIST
- Jorgen Christiansen, CERN
- Thomas Kirk, VESTAS.
- Jordi Aubert, FICOSA



The Universitat Politècnica de Catalunya and the Electronic Engineering Department invites you to participate in this event that will take place the 16th of October 2009 in the Auditori Vèrtex. Confirmation is required by sending an e-mail to: mcrus@eel.upc.edu

Electronic Engineering Department - UPC

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